FIG. 1

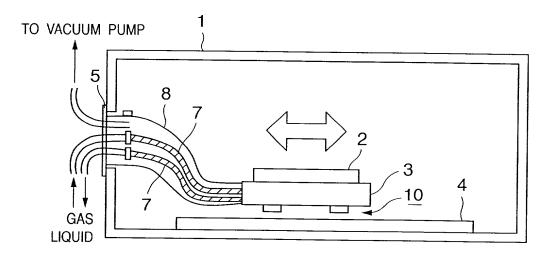


FIG. 2

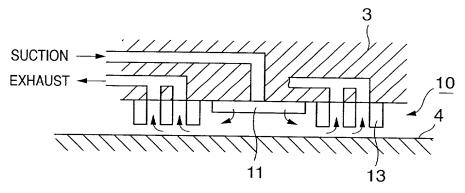
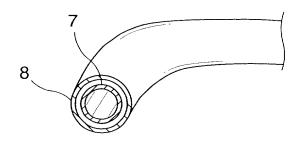
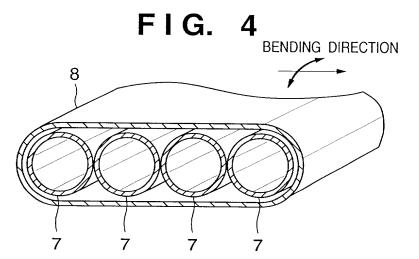
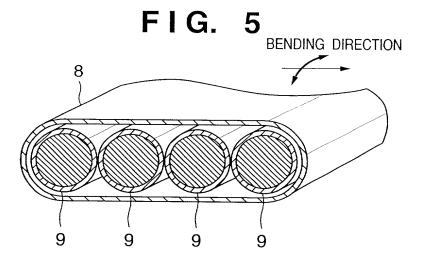


FIG. 3







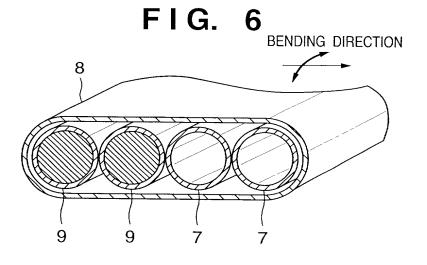


FIG. 7

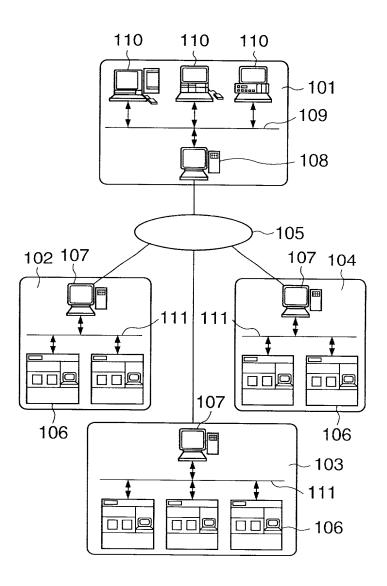


FIG. 8

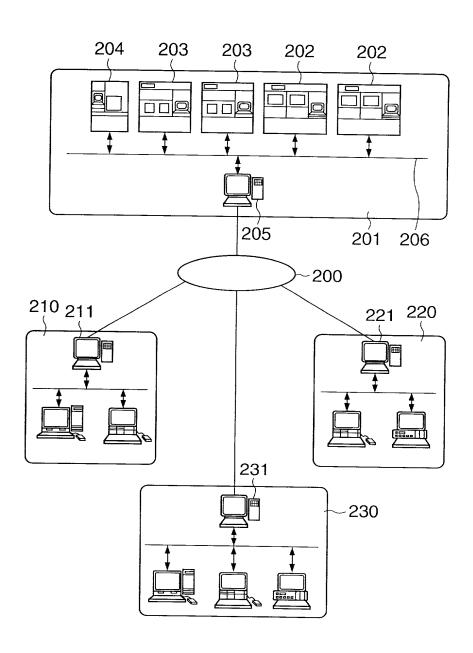


FIG. 9

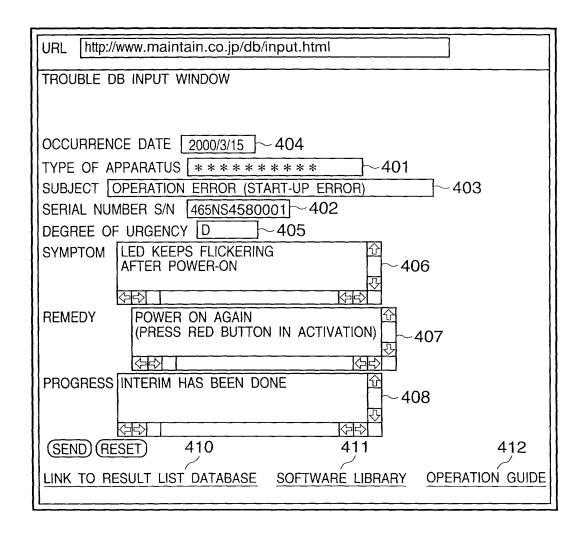
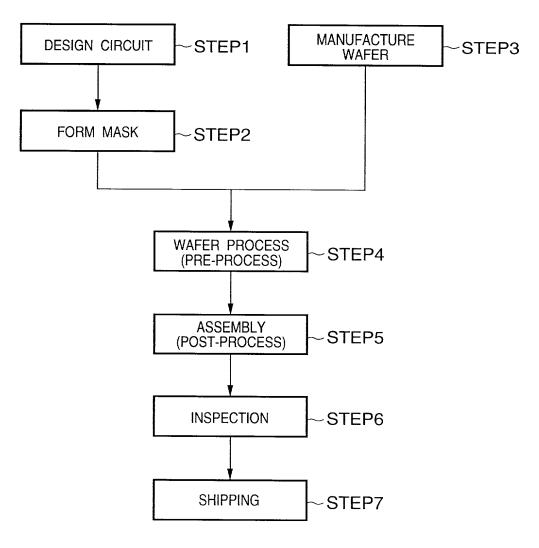


FIG. 10



SEMICONDUCTOR DEVICE MANUFACTURING FLOW

FIG. 11

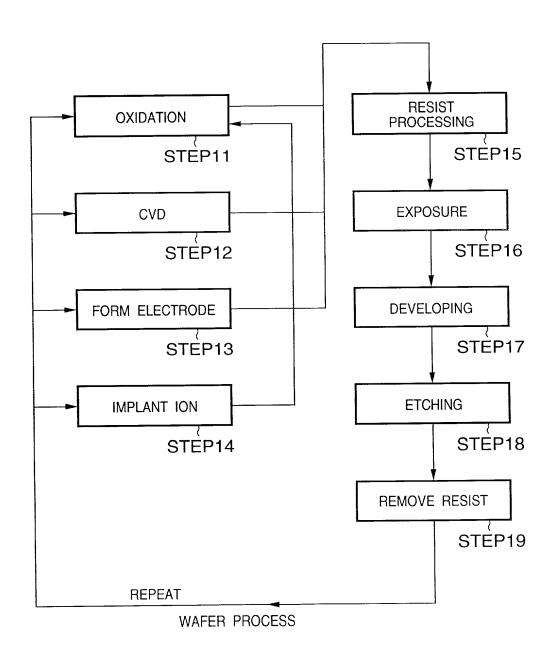


FIG. 12

